



(12) **EUROPEAN PATENT APPLICATION**

(43) Date of publication:
 17.11.1999 Bulletin 1999/46

(51) Int Cl.⁶: **H01S 3/25**

(21) Application number: **99303310.9**

(22) Date of filing: **28.04.1999**

(84) Designated Contracting States:
AT BE CH CY DE DK ES FI FR GB GR IE IT LI LU
MC NL PT SE
 Designated Extension States:
AL LT LV MK RO SI

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(30) Priority: **06.05.1998 US 73598**

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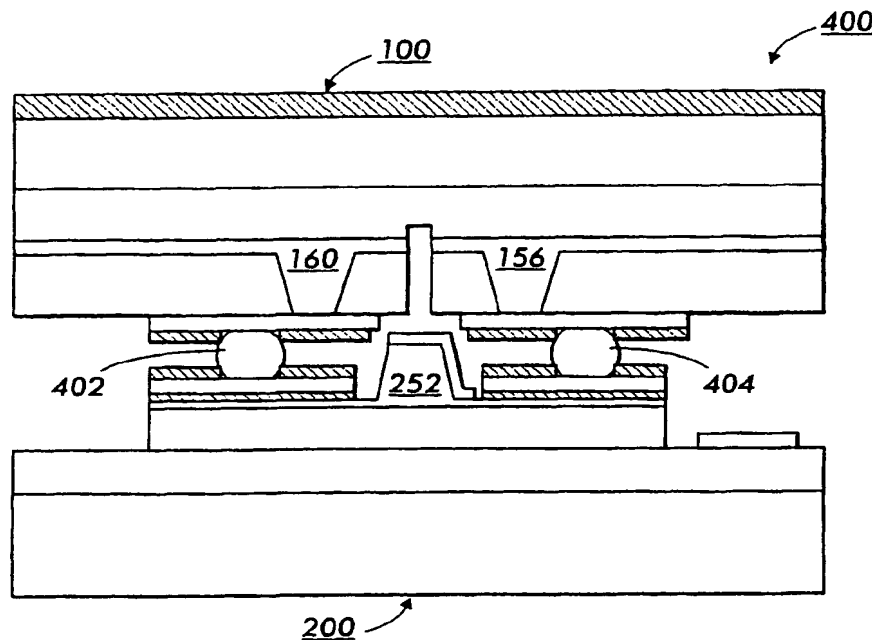
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(54) **Multiple wavelength laser arrays**

(57) A side by side red/IR laser structure (160,156) is flip chip bonded by solder balls (402,404) to a blue

laser structure (252) to form a red/blue/IR hybrid integrated laser structure (400).

FIG. 7



Description

[0001] This invention relates to a multiple wavelength laser structure and, more particularly, to a multiple wavelength laser array structure fabricated by laser structures on two different substrates.

[0002] Addressable monolithic multi-wavelength light sources, especially arrays that can simultaneously emit different wavelength light beams from different laser elements in the monolithic structure, are useful in a variety of applications such as color printing, full color digital film recording, color displays, and other optical recording and storage system applications.

[0003] The performance of many devices, such as laser printers and optical memories, can be improved by the incorporation of multiple laser beams. For example, laser printers which use multiple beams can have higher printing speeds and/or better spot acuity than printers which use only a single beam.

[0004] In these and many applications, closely spaced laser beams of different wavelengths are desirable.

[0005] One way to obtain closely spaced laser beams is to form multiple laser emission sites, or laser stripes, on a common substrate. While this enables very closely spaced beams, prior art monolithic laser arrays typically output laser beams at only one wavelength.

[0006] Various techniques are known in the prior art for producing different wavelength laser beams from a monolithic laser array. For example, it is well known that a small amount of wavelength difference can be obtained by varying the drive conditions at each lasing region. However, this easily achievable but small wavelength difference is insufficient for most applications.

[0007] Ideally, for most desired applications, the laser elements should emit light of different, widely spaced, wavelengths. In a preferred monolithic structure, the laser elements would emit light across a widely spaced spectrum from infrared to red to blue wavelengths. One problem is that laser sources of different wavelengths require different light emission active layers; i.e. nitride semiconductor layers such as InGaAlN for blue lasers, arsenide semiconductor layers such as AlInGaAs for infrared and phosphide semiconductor layers such as GaInP for red lasers.

[0008] One method of achieving these larger wavelength separations is to grow a first set of active layers on a substrate to form a first lasing element which outputs light at one wavelength, and then to etch and regrow a second set of active layers next to the first to form a second lasing element at a second wavelength. However, this method requires separate crystal growths for each lasing element, something which is not easily performed. Furthermore, the arsenide and phosphide semiconductor structures of infrared and red lasers use a different, non-compatible substrate with the nitride semiconductor structures of blue lasers. Lattice mismatching between semiconductor layers will result in

poor or non-existent performance of one or more of the laser structures.

[0009] Another technique for obtaining different wavelength laser beams from a monolithic laser array is to use stacked active regions. A stacked active region monolithic array is one in which a plurality of active regions are sandwiched between common cladding layers. Each active region is comprised of a thin volume that is contained within a laser stripe. The laser stripes contain different numbers of active regions that emit laser beams at different wavelengths.

[0010] In a stacked active region monolithic laser array, current flows in series through the stacked active regions. The active region with the lowest bandgap energy will lase, thereby determining the wavelength of the laser beam output from that part of the array. To provide another wavelength output, the previously lowest bandgap energy active region is removed from part of the array and current is sent through the remaining stacked regions.

[0011] A major problem with stacked active region monolithic laser arrays is that they have been difficult to fabricate, even with just arsenide and phosphide semiconductor layers. The addition of nitride semiconductor layers makes optical performance nearly impossible and impractical in any real world applications.

[0012] Side by side laser arrays can not only output closely spaced laser beams of different wavelengths, but beneficially the output laser beams are aligned.

[0013] According to this invention a side-by-side red/IR laser structure is bonded to a blue laser structure to form a red/blue/IR hybrid integrated laser structure. Laser array structures having elements of different wavelength can be fabricated by this method in semiconductor material systems which are incompatible for etch and regrowth manufacturing techniques.

[0014] Particular embodiments of laser structures in accordance with this invention will now be described with reference to the accompanying drawings; in which:-

Figure 1 is a cross-sectional side view of the semiconductor layers of the Red/IR side by side laser structure of the present invention;

Figure 2 is a cross-sectional side view of the semiconductor layers of the Red/IR side by side ridge waveguide laser structure of the present invention;

Figure 3 is a cross-sectional side view of the semiconductor layers of the Blue laser structure of the present invention;

Figure 4 is a cross-sectional side view of the semiconductor layers of the Blue ridge waveguide laser structure of the present invention;

Figure 5 is a cross-sectional side view of the solder bumps on the contact pads of the Red/IR laser structure and the Blue laser structure for flip-chip bonding of the present invention;

Figure 6 is a cross-sectional side view of the semiconductor layers of the Red/IR side by side laser

structure and the Blue laser structure prior to flip-chip bonding of the present invention; and, Figure 7 is a cross-sectional side view of the semiconductor layers of the Red/Blue/IR stack laser structure formed by flip-chip bonding of the present invention.

[0015] The present invention involves fabricating a Red/IR side by side laser structure, fabricating a Blue laser structure, flipping over the Red/IR stack laser structure to flip-chip bond the Red/IR side by side laser structure to the Blue laser structure and forming the resulting integrated Red/Blue/IR laser structure.

[0016] Reference is now made to Figure 1 which illustrates a Red/IR side by side laser structure 100 in accordance to the present invention.

[0017] As shown in Figure 1, an n-type $\text{Al}_{0.5}\text{In}_{0.5}\text{P}$ lower cladding layer 104 is grown on an n-type GaAs substrate 102 using a well-known epitaxial deposition process commonly referred to as metal-organic chemical vapor deposition ("MOCVD"). Other deposition processes such as liquid phase epitaxy ("LPE"), molecular beam epitaxy ("MBE"), or other known crystal growth processes can also be used. The aluminum mole fraction and doping level of the lower cladding layer 104 range from 50 percent and 1 to $5 \times 10^{18} \text{ cm}^{-3}$ respectively. The thickness of the AlInP cladding layer 104 is approximately one micron (μm). The doping level of the n-type GaAs substrate 102 is approximately $5 \times 10^{18} \text{ cm}^{-3}$ or higher.

[0018] An undoped $\text{Al}_{0.4}\text{Ga}_{0.6}\text{As}$ lower confinement layer 106 is deposited on the cladding layer 104. The lower confinement layer 106 has an aluminum content of about 40% and a thickness of about 120 nanometers. After this lower confinement layer 106 has been deposited, an $\text{In}_{0.15}\text{Al}_{0.15}\text{Ga}_{0.7}\text{As}$ active layer 108 is deposited. The active layer 108 should emit light at approximately 835 nanometers. The active layer 108 may be a single quantum well, a multiple quantum well, or a layer with thickness greater than that of a quantum well. The thickness of a quantum well typically ranges from five to twenty nanometers and in this example is 8 nanometers. Upon the active layer 108 is deposited an undoped $\text{Al}_{0.4}\text{Ga}_{0.6}\text{As}$ upper confinement layer 110. The aluminum content of this confinement layer 110 is typically 40% and a thickness of about 120 nanometers. The lower and upper confinement layers, 106 and 110, together with the active layer 108, form the active region 112 for a laser structure with a lower threshold current and a smaller optical divergence.

[0019] Upon the upper confinement layer 110 in the infrared laser structure 120 is deposited a $\text{Al}_{0.5}\text{In}_{0.5}\text{P}$ upper cladding layer 114 of about one micron thickness. Typically, this upper cladding layer 114 has an aluminum content of 50% and a magnesium doping level of $5 \times 10^{18} \text{ cm}^{-3}$.

[0020] Upon the upper p-type $\text{Al}_{0.5}\text{In}_{0.5}\text{P}$ cladding layer 114 is deposited a barrier reduction $\text{Ga}_{0.5}\text{In}_{0.5}\text{P}$ layer 116, which typically has a thickness of 50 nanometers,

and a magnesium doping level of approximately $5 \times 10^{18} \text{ cm}^{-3}$. Upon this barrier reduction GaInP layer 116 is deposited a p+-GaAs cap layer 118, which typically is 100 nanometers thick with a magnesium doping of $1 \times 10^{19} \text{ cm}^{-3}$.

[0021] These semiconductor layers 102 to 118 form the infrared laser structure 120.

[0022] The laser structure 100 of Figure 1 with the epitaxially deposited infrared laser structure 120 is then covered with a silicon nitride (SiN_x) or silicon oxide (SiO_x) layer (not shown). Stripe windows are then opened in the silicon nitride or oxide layer by photolithography and plasma etching. The stripe windows (not shown) are 300 microns wide with 500 micron spacing. Wet chemical etching with citric acid ($\text{C}_6\text{H}_8\text{O}_7 \cdot \text{H}_2\text{O}$), hydrogen dioxide (H_2O_2) and bromine acid (HBr) is then used to etch away the infrared laser structure 120 layers of cap layer 118, barrier reduction layer 116, upper cladding layer 114, upper confinement layer 110, active layer 108, lower confinement layer 106 and lower cladding layer 104 in the window region down to the n-GaAs substrate 102. The red laser structure will then be grown on the substrate in the window region. Once the red laser structure has been deposited, the silicon nitride or oxide layer on top of the remaining infrared laser structure 120 will be removed by plasma etching.

[0023] An n-type $\text{Al}_{0.5}\text{In}_{0.5}\text{P}$ lower cladding layer 124 is deposited on the n-type GaAs substrate 102. The aluminum mole fraction and doping level of the lower cladding layer 124 range from 50 percent and 1 to $5 \times 10^{18} \text{ cm}^{-3}$ respectively. The thickness of the AlInP cladding layer 124 is approximately one micron (μm).

[0024] An undoped $\text{In}_{0.5}(\text{Al}_{0.6}\text{Ga}_{0.4})_{0.5}\text{P}$ lower confinement layer 126 is deposited on the lower cladding layer 124. The lower confinement layer 126 has an aluminum content of about 30% and a thickness of about 120 nanometers. After this lower confinement layer 126 has been deposited, an $\text{In}_{0.6}\text{Ga}_{0.4}\text{P}$ active layer 128 is deposited. The active layer 128 will emit light at approximately 670 nanometers. The active layer 128 may be a single quantum well, a multiple quantum well, or a layer with thickness greater than that of a quantum well. The thickness of a quantum well typically ranges from five to twenty nanometers and in this example is 8 nanometers. Upon the active layer 128 is deposited an undoped $\text{In}_{0.5}(\text{Al}_{0.6}\text{Ga}_{0.4})_{0.5}\text{P}$ upper confinement layer 130. The aluminum content of this confinement layer 130 is typically 30% and a thickness of about 120 nanometers. The lower and upper confinement layers, 126 and 130, together with the active layer 128, form an active region 132 for a laser structure with a lower threshold current and a smaller optical divergence.

[0025] Upon the upper confinement layer 130 in the red laser structure 140 is deposited a $\text{Al}_{0.5}\text{In}_{0.5}\text{P}$ upper cladding layer 134 of about one micron thickness. Typically, this upper cladding layer 134 has an aluminum content of 50% and a magnesium doping level of $5 \times 10^{18} \text{ cm}^{-3}$.

[0026] Upon the upper p-type $\text{Al}_{0.5}\text{In}_{0.5}\text{P}$ cladding layer 134 is deposited a barrier reduction $\text{Ga}_{0.5}\text{In}_{0.5}\text{P}$ layer 136, which typically has a thickness of 50 nanometers, and a magnesium doping level of approximately $5 \times 10^{18} \text{ cm}^{-3}$. Upon this barrier reduction GaInP layer 136 is deposited a p+-GaAs cap layer 138, which typically is 100 nanometers thick with a magnesium doping of $1 \times 10^{19} \text{ cm}^{-3}$.

[0027] These semiconductor layers 102 and 124 to 138 form the red laser structure 140.

[0028] The infrared laser structure 120 and the red laser structure 140 have the same substrate 102. The upper and lower cladding layers and the barrier reduction and cap layers for the infrared laser structure 120 and the red laser structure 140 are the same semiconductor materials with the same thicknesses and the same doping or nondoping. Since the upper and lower confinement layers and the active layers of the infrared laser structure 120 and the red laser structure 140 are the same thickness (although different semiconductor materials), the active regions for light emission of the two laser structures 120 and 140 within the laser structure 100 are the same height and parallel. Similarly, the electrode contacts for the infrared laser structure 120 and the red laser structure 140 on the cap layers 118 and 138 will be the same height and parallel.

[0029] The infrared laser structure 120 and the red laser structure 140 of the side by side laser structure 100 can have accurate lateral spacing of 50 microns or less with zero or minimum vertical spacing between the side by side laser structures.

[0030] Ridge waveguides can be fabricated for the IR/Red side by side laser structure 100 of Figure 2.

[0031] After all the semiconductor layers of the semiconductor structure 100 shown in Figure 1 have been deposited, a silicon nitride (SiN_x) or silicon oxide (SiO_2) layer is deposited on the upper surface of the cap layer 118 of the infrared laser structure 120 and on the upper surface of the cap layer 138 of the red laser structure 140 in Figure 2. Dual stripes of 50 micron spacing are patterned on the cap layers 118 and 138 leaving open windows between the stripes.

[0032] A mixed bromine methanol ($\text{Br}:\text{CH}_3\text{OH}$) solution etches through the unmasked portions of the cap layer 118 and barrier reduction layer 116 of the infrared laser structure 120 leaving a small mesa 150 of masked, and thus unetched, cap layer 118 and barrier reduction layer 116 between the open window grooves.

[0033] The mixed bromine methanol ($\text{Br}:\text{CH}_3\text{OH}$) solution also etches through the unmasked portions of the cap layer 138 and barrier reduction layer 136 of the red laser structure 140 leaving a small mesa 152 of masked, and thus unetched, cap layer 138 and barrier reduction layer 136 between the open window grooves.

[0034] Phosphoric acid (H_3PO_4) then etches the unmasked portions of the 1 micron thick p-type $\text{Al}_{0.5}\text{In}_{0.5}\text{P}$ upper cladding layer 114 on either side of the mesa 150 down to a 0.35 micron thick p-type $\text{Al}_{0.5}\text{In}_{0.5}\text{P}$ upper

cladding layer 114 above the active region 112 of layers 110, 108 and 106 using a timed etching technique.

[0035] The phosphoric acid (H_3PO_4) also etches the unmasked portions of the 1 micron thick p-type $\text{Al}_{0.5}\text{In}_{0.5}\text{P}$ upper cladding layer 134 on either side of the mesa 152 down to a 0.35 micron thick p-type $\text{Al}_{0.5}\text{In}_{0.5}\text{P}$ upper cladding layer 134 above the active region 132 of layers 130, 128 and 126 using a timed etching technique.

[0036] Reactive ion etching may be used in place of wet chemical etching.

[0037] Upon completion of etching as shown in Figure 2, the silicon nitride stripes are removed.

[0038] The remaining $\text{Al}_{0.5}\text{In}_{0.5}\text{P}$ upper cladding layer 154 under the mesa 150 forms the ridge waveguide 156 for optical confinement of the light emitted from the active region of the infrared laser structure 120.

[0039] The remaining $\text{Al}_{0.5}\text{In}_{0.5}\text{P}$ upper cladding layer 158 under the mesa 152 forms the ridge waveguide 160 for optical confinement of the light emitted from the active region of the red laser structure 140.

[0040] An isolation groove 162 is etched between the infrared laser structure 120 and the red laser structure 140 down to the substrate 102 to provide electrical and thermal isolation between the two laser structures.

[0041] After the removal of the silicon nitride stripes, a Ti-Au p-contact 164 can be deposited on the upper surface of the cap layer 118, the contact layer 116 and the upper cladding layer 114 for the infrared laser structure 120. The ridge waveguide 156 is confined by the metal p-contact 164. A Ti-Au p-contact 166 can be deposited on the upper surface of the cap layer 138, the contact layer 136 and the upper cladding layer 134 for the red laser structure 140. The ridge waveguide 160 is confined by the metal p-contact 166. An Au:Ge n-contact 168 can be deposited on the bottom surface of the substrate 102, common to both the infrared laser structure 120 and the red laser structure 140.

[0042] The infrared laser structure 120 with its metal confined ridge waveguide 156 and the red laser structure 140 with its metal confined ridge waveguide 160 will each emit a single transverse mode light emission.

[0043] The IR and red laser structures, although closely spaced side by side, are independently addressable with fast switching of less than 3 nanoseconds.

[0044] The IR/Red side by side laser structure 100 of Figure 2 is an edge emitting array. Conventional facets (not shown) are provided on the edge of the laser structure 100. The infrared laser structure 120 will emit light of infrared wavelength from the active region 112 including the active layer 108 through the edge of the laser structure. The red laser structure 140 will emit light of red wavelength from the active region 132 including the active layer 128 through the edge of the laser structure.

[0045] Reference is now made to Figure 3 which illustrates a Blue laser structure 200 in accordance to the

present invention.

[0046] The Blue laser structure 200 has a sapphire (Al_2O_3) substrate 202. Upon the substrate 200 is deposited an undoped GaN buffer layer 204 which has a thickness of 30 nanometers. A n-GaN buffer layer 206 is deposited on the undoped buffer layer 204. The n-buffer layer is silicon doped at $1 \times 10^{18} \text{ cm}^{-3}$ and has a thickness of 4 microns. A n- $\text{In}_{0.05}\text{Ga}_{0.95}\text{N}$ stress reduction layer 208 is deposited on the n-GaAs buffer layer 206. The InGaN stress reduction layer 208 has a thickness of 0.1 microns and a silicon doping level of $1 \times 10^{18} \text{ cm}^{-3}$. Upon the InGaN stress reduction layer 208 is deposited a n- $\text{Al}_{0.08}\text{Ga}_{0.92}\text{N}$ lower cladding layer 210. The n-cladding layer 210 is silicon doped at $1 \times 10^{18} \text{ cm}^{-3}$ and has a thickness of 0.5 microns.

[0047] An n-GaN waveguiding layer 212 is deposited on the n-AlGaIn lower cladding later 210. The GaN waveguiding layer 212 is 0.1 microns thick and is silicon doped at $1 \times 10^{18} \text{ cm}^{-3}$. An $\text{In}_{0.15}\text{Ga}_{0.85}\text{N}/\text{In}_{0.02}\text{Ga}_{0.98}\text{N}$ multiple quantum well active layer 214 is deposited on the waveguiding layer 212. The $\text{In}_{0.15}\text{Ga}_{0.85}\text{N}/\text{In}_{0.02}\text{Ga}_{0.98}\text{N}$ multiple quantum well active layer 214 has 3 to 20 quantum wells and is about 50 nanometers thick and emits light at 410 to 430 nanometers. A p- $\text{Al}_{0.2}\text{Ga}_{0.8}\text{N}$ carrier confinement layer 216 is deposited on the active layer 214. The p-AlGaIn carrier confinement layer 216 is magnesium doped at $5 \times 10^{19} \text{ cm}^{-3}$ and has a thickness of 0.02 microns. An p-GaN waveguiding layer 218 is deposited on the p-AlGaIn carrier confinement later 216. The GaN waveguiding layer 218 is 0.1 microns thick and is magnesium doped at $5 \times 10^{19} \text{ cm}^{-3}$. The waveguiding layers 212 and 218, together with the confinement layer 216 and the active layer 214 form the active region 219 of the laser structure.

[0048] A p- $\text{Al}_{0.08}\text{Ga}_{0.92}\text{N}$ upper cladding layer 220 is deposited on the waveguiding layer 218. The p-cladding layer 220 is magnesium doped to $5 \times 10^{19} \text{ cm}^{-3}$ and has a thickness of 0.5 microns. A p-GaN contact layer 222 is deposited on the p-confinement layer 220. The contact layer 222 is magnesium doped to $5 \times 10^{19} \text{ cm}^{-3}$ and has a thickness of 0.5 microns.

[0049] Ridge waveguides can be fabricated for the blue laser structure 200 of Figure 4.

[0050] After all the semiconductor layers of the semiconductor structure 200 shown in Figure 3 have been deposited, a photoresist mask is deposited on the upper surface of the contact layer 222 of the blue laser structure 200. A stripe is patterned in the photoresist leaving an open section 250 of the contact layer 222. Chemically assisted ion beam etching ("CAIBE") then etches the unmasked portion 250 of the contact layer 222 through the contact layer 222, the p-upper cladding layer 220, the p-waveguiding layer 218, the p-carrier confinement layer 216, the multiple quantum well active layer 214, the n-waveguiding layer 212, the n-lower cladding layer 210, the n-stress reduction layer 208 to the surface 252 of the n-GaAs buffer layer 206. The photoresist mask is then removed.

[0051] Then, another photoresist mask is deposited on the upper surface of the contact layer 222 and the surface 252 of the exposed n-buffer layer 206 of the blue laser structure 200. Dual stripes of 50 micron spacing are patterned on the contact layer 222 leaving open windows between the stripes.

[0052] Chemically assisted ion beam etching then etches through the unmasked portions of the contact layer 222 leaving a small mesa 254 of masked, and thus unetched, contact layer 222 between the open window grooves.

[0053] Chemically assisted ion beam etching continues to etch the unmasked portions of the 0.5 micron thick p-type $\text{Al}_{0.08}\text{Ga}_{0.92}\text{N}$ upper confinement layer 220 on either side of the mesa 254 down to a 0.35 micron thick p-type $\text{Al}_{0.08}\text{Ga}_{0.92}\text{N}$ upper cladding layer 220 above the active region 219 of layers 212, 214, 216 and 218 using a timed etching technique.

[0054] Upon completion of etching, the photoresist mask is removed.

[0055] The remaining p-type $\text{Al}_{0.08}\text{Ga}_{0.92}\text{N}$ upper cladding layer 220 under the mesa 254 forms the ridge waveguide 256 for optical confinement of the light emitted from the active region of the blue laser structure 200.

[0056] After the removal of the silicon nitride stripes, a Ti-Au p-contact 258 can be deposited on the upper surface of the upper confinement layer 220, the ridge waveguide 256 and the mesa 254 of the contact layer 222 for the blue laser structure 200. The ridge waveguide 256 is confined by the metal p-contact 258. A Ti-Au n-contact 260 can be deposited on the surface 252 of the n-GaAs buffer layer 206.

[0057] The blue laser structure 200 with its metal confined ridge waveguide 256 will emit a single transverse mode light emission.

[0058] The blue laser structure 200 of Figure 4 is an edge emitting array. Conventional facets (not shown) are provided on the edge of the laser structure 200. The blue laser structure 200 will emit light of blue wavelength from the active region 219 including the active layer 214 through the edge of the laser structure.

[0059] The IR/Red side by side ridge waveguide laser structure 100 of Figure 2 will be flip-chip bonded to the blue ridge waveguide laser structure 200 of Figure 4 to form an integrated IR/Red/Blue laser structure.

[0060] Flip-chip bonding involves soldering two semiconductor structures together in a face to face relationship to form a single semiconductor structure. Typically, solder bumps are formed on contact pads on the surfaces of each semiconductor structure. One structure or "chip" is "flipped over" to face the other structure or "chip". The solder bumps on both semiconductor structures are aligned, then pressed together while the structures are heated. The two solder bumps fuse together into one solder bump, joining the two semiconductor structures into one integrated semiconductor structure. The surface tension of the liquid solder metal pulls the two laser structures into very exact alignment.

[0061] As shown in the illustrative example of Figure 5, two contacts pads 300 and 302 for the solder bumps are formed on the surface of the p-type $\text{Al}_{0.5}\text{In}_{0.5}\text{P}$ upper cladding layers 114 and 134, respectively, of the Red/IR laser structure 100. The contact pad 300 overlaps the ridge waveguide 156 and the upper cladding layer 114 of the IR laser structure 120. The contact pad 302 overlaps the ridge waveguide 160 and the upper cladding layer 134 of the red laser structure 140.

[0062] The contact pads are formed by metal evaporation of successive metal layers: a 30 nanometer thick layer of Ti, a 50 nanometer thick layer of Au, a 30 nanometer thick layer of Ti and a 200 nanometer thick layer of Ni. The contact pad 300 is separated and isolated, physically and electrically, from both the ridge waveguide 156 and the p-contact 164 of the IR laser structure 120, which are also on the surface of the p-type $\text{Al}_{0.5}\text{In}_{0.5}\text{P}$ upper cladding layer 114. The contact pad 302 is separated and isolated, physically and electrically, from both the ridge waveguide 160 and the p-contact 166 of the Red laser structure 140, which are also on the surface of the p-type $\text{Al}_{0.5}\text{In}_{0.5}\text{P}$ upper cladding layer 134.

[0063] A SiON dielectric layer 304 is deposited by plasma-enhanced chemical vapor deposition ("PECVD") or by electron beam evaporation at 275° C over most of both contact pads 300 and 302. The SiON dielectric layer 304 will serve as a solder stop and to further electrically isolate the contact pads 300 and 302 from the p-contacts 164 and 166. The Ti/Au layers of the contact pads next to the upper cladding layer are the standard laser ohmic contact metals. The overlying Ti/Ni layers of the contact pads fulfill the somewhat conflicting requirements of acting as a low contact resistance interface between the upper cladding layer thin film metallization and the 10 to 12 micron thick Pb/Sn solder bumps and serving as a solder anti-leach layer. Furthermore, Ni forms a surface oxide stable enough to insure good SiON adhesion.

[0064] The dielectric film 304 is then patterned by using standard photolithographic procedures. A photoresist mask (not shown) is aligned with the ridge waveguides 156 and 160 and the underlying laser stripes and formed on the surface of the SiON dielectric layer 304. The unmasked portions of the SiON dielectric layer 304 are plasma etched to form contact holes 306 and 308 of 50 to 90 μm diameter. The contact hole 306 will be etched down through the dielectric film 304 to the contact pad 300. The contact hole 308 will be etched down through the dielectric film 304 to the contact pad 302. This etching step determines the final alignment between the two laser semiconductor structures and also defines the wettable contact pad area.

[0065] A 20 nanometer thick Sn layer 310 is deposited on the contact pad 300 through the contact hole 306. A 20 nanometer thick Sn layer 312 is deposited on the contact pad 302 through the contact hole 308.

[0066] A 10 micron thick layer of photoresist 314 is

then deposited on the surface in a three layer pattern with a retrograde profile hole 316 developed and aligned with the contact hole 306 and a retrograde profile hole 318 developed and aligned with the contact hole 308.

5 [0067] PbSn solder is evaporated in an e-beam evaporator and the subsequent lift-off results in a PbSn disk 320 deposited on the Sn layer 310 on the contact pad 300 and a PbSn disk 322 deposited on the Sn layer 312 on the contact pad 302. The 10 μm thick PbSn disks are slightly larger in diameter than the wettable contact pads 300 and 302. The photoresist 314 is then removed.

10 [0068] The solder disks 320 and 322 are reflowed using flux and heated to temperature of 220° C to pull back onto the wettable contact pad areas 300 and 302, respectively, and form solder bumps 324 and 326 of hemispherical shape. Solder bump 324 is on the Sn layer 310 on the contact pad 300 and solder bump 326 is on the Sn layer 312 on the contact pad 302. The Sn layer is alloyed with the PbSn solder to form a good electrical and mechanical connection.

15 [0069] The contact pads and solder bumps on the Blue laser structure 200 are formed by the same process. A SiON dielectric layer 354 covers most of both Ti/Au/Ti/Ni contact pads 350 and 352 on the upper surface of the upper confinement layer 220 for the blue laser structure 200. Solder bump 358 is on the Sn layer 356 on the contact pad 350 and solder bump 362 is on the Sn layer 360 on the contact pad 352.

20 [0070] The Figures and text show one row of solder bumps. Across the entire face of the laser structures are rows and rows of solder bumps to provide a good mechanical and electrical connection between the two laser structures.

25 [0071] The Red/IR laser structure 100 with its solder bumps 324 and 326 on contact pads 300 and 302 and the Blue laser structure 200 with its solder bumps 358 and 362 on contact pads 350 and 352 are separately scribed and cleaned by standard means. Tacky flux is applied to the surface of the solder bumps on both laser structures.

30 [0072] As shown in Figure 6, the GaAs Red/IR structure 100 flip-chips on top of the InGaN/sapphire Blue laser structure 200 with a positioning accuracy of $\pm 10 \mu\text{m}$ using a flip-chip aligner bonder. The solder bumps 326 and 324 of the Red/IR laser structure 100 are attached to the solder bumps 358 and 352 of the Blue laser structure 200, respectively, and held in place by the flux.

35 [0073] The hybrid integrated Red/Blue/IR laser structure 400 is heated to temperature of 220° C again. During a second reflow, the two solder bumps 326 and 358 become one solder bump 402 and the two solder bumps 324 and 352 become one solder bump 404 flip-chip bonding the two separate laser structures 100 and 200 into one integrated laser structure 400 as shown in Figure 7. During this bonding process, the Red/IR laser structure and the Blue laser structure are pulled into very accurate alignment through the self-aligned surface ten-

sion between the Pb/Sn solder bumps.

[0074] The blue laser ridge 256 is placed in the center between the red laser ridge 160 and the IR laser ridge 156 in order to achieve close spacing in the lateral direction. The spacing in the vertical direction depends on the bump size; for solder bump hemispheres with exactly 50, 70, and 90 μm diameter, the chip separations are 30, 40 and 50 μm respectively. Since through the hybrid bonding scheme, there is only very low thermal cross-talk between the blue and the Red/IR lasers, the lateral spacing is essentially limited by the spacing between the red laser and the IR laser. If the separation between the Red laser and the IR laser is 20 μm , the separation between the Blue laser and the Red laser (or the IR laser) is only 10 μm . The separation in the vertical direction depends upon the bump diameter. In certain applications, such as a print-head, the vertical separation can be virtually zero because of an electronically adjustable delay in the Red/IR lasers. The close separation will be beneficial for high-speed and high-resolution printers.

[0075] Flip-chip bonding creates a non-lattice matched heterostructure, the hybrid integrated laser structure 400 in this embodiment. This integration of the Red/Blue/IR laser structure 400 provides a closely spaced, precisely spaced structure of three different, widely spaced wavelength laser sources, necessary for precision optical systems.

[0076] In summary, the use of flip-chip bonding techniques paves the way for multi-wavelength semiconductor lasers fabricated in material systems which are incompatible for etch and regrowth techniques. Such multi-wavelength devices have interesting applications in high-speed and high-resolution color-printing or scanning.

Claims

1. An integrated edge-emitting semiconductor laser structure comprising:

a first laser structure having
 a first substrate;
 a first plurality of semiconductor layers formed on said first substrate;
 one or more of said first plurality of semiconductor layers forming a first active region;
 first bonding means on one of said first plurality of semiconductor layers;
 a second laser structure having
 a second substrate;
 a second plurality of semiconductor layers formed on said second substrate;
 one or more of said second plurality of semiconductor layers forming a second active region;
 second bonding means on one of said second plurality of semiconductor layers, said second

bonding means bonded to at least one of said first bonding means;

a third plurality of semiconductor layers formed on said second substrate;

one or more of said third plurality of semiconductor layers forming a third active region;

third bonding means on one of said third plurality of semiconductor layers, said third bonding means bonded to at least one of said first bonding means;

a first contact and a second contact which enable biasing of said first active region for emission of light of said first wavelength,

a third contact and a fourth contact which enable biasing of said second active region for emission of light of said second wavelength, and

a fifth contact and a sixth contact which enable biasing of said third active region for emission of light of said third wavelength.

2. An integrated edge-emitting semiconductor laser structure according to claim 1, wherein said first wavelength, said second wavelength, and said third wavelength are different.

3. An integrated edge-emitting semiconductor laser structure according to claim 1 or 2, wherein said first wavelength is in the blue range, said second wavelength is in the infrared range and said third wavelength is in the red range.

4. An integrated edge-emitting semiconductor laser structure according to any one of the preceding claims, wherein the bonding means are flip-chip bonding means.

5. An integrated edge-emitting semiconductor laser structure according to any one of the preceding claims in which both substrates have two pluralities of semiconductor layers each including an active an active region formed on them, so that the laser has four sources of light and emits light of two different wavelengths, three different wavelengths or four different wavelengths.

6. A method of fabricating an integrated edge-emitting semiconductor laser structure comprising the steps of:

fabricating a side by side laser structure having a first laser structure for emitting light of a first wavelength and a second laser structure for emitting light of a second wavelength, said first and second laser structure having a first and second bonding means,
 fabricating a third laser structure for emitting light of a third wavelength, said third laser struc-

ture having a third bonding means,
flip chip bonding said first and second bonding
means of said side by side laser structure to
said third bonding means of said third laser
structure, and 5
forming contacts which enable independently
addressable biasing of said first laser structure
to emit light of said first wavelength, said sec-
ond laser structure to emit light of said second
wavelength and said third laser structure to 10
emit light of said third wavelength.

7. A method of fabricating an integrated edge-emitting
semiconductor laser structure according to claim 6,
wherein said first wavelength is in the red range, 15
said second wavelength is in the infrared range and
said third wavelength is in the blue range.

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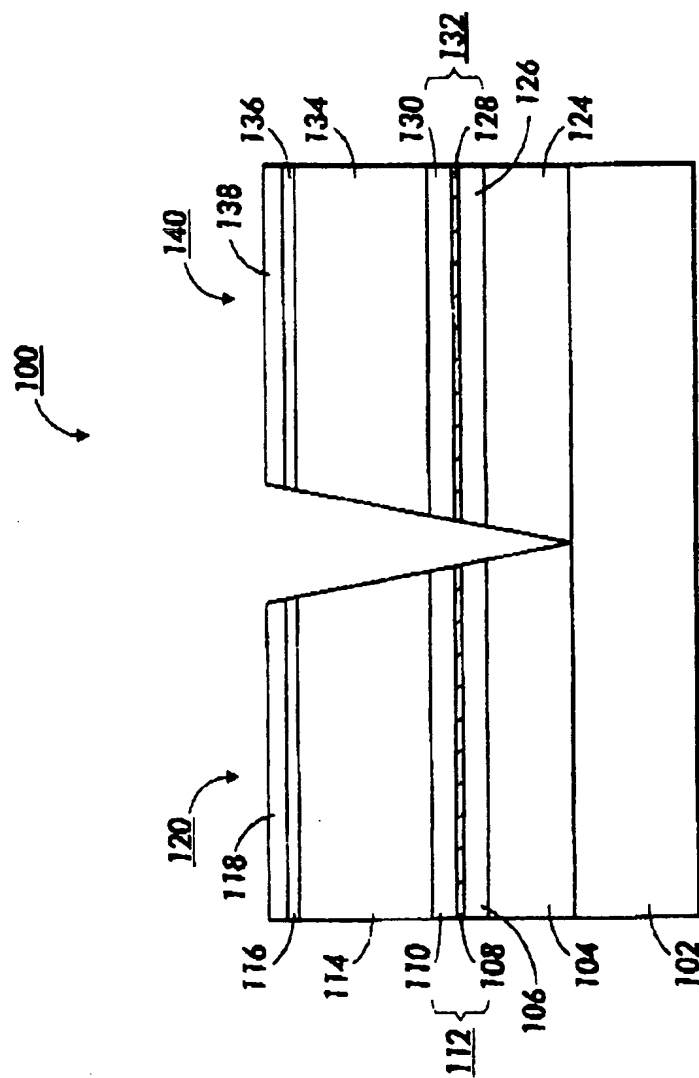


FIG. 1

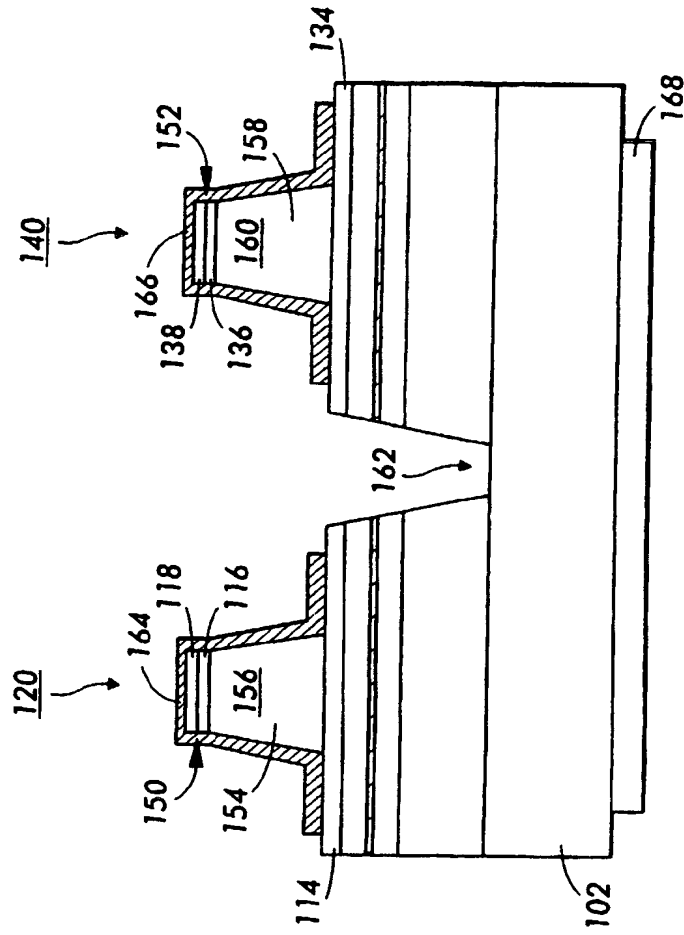


FIG. 2

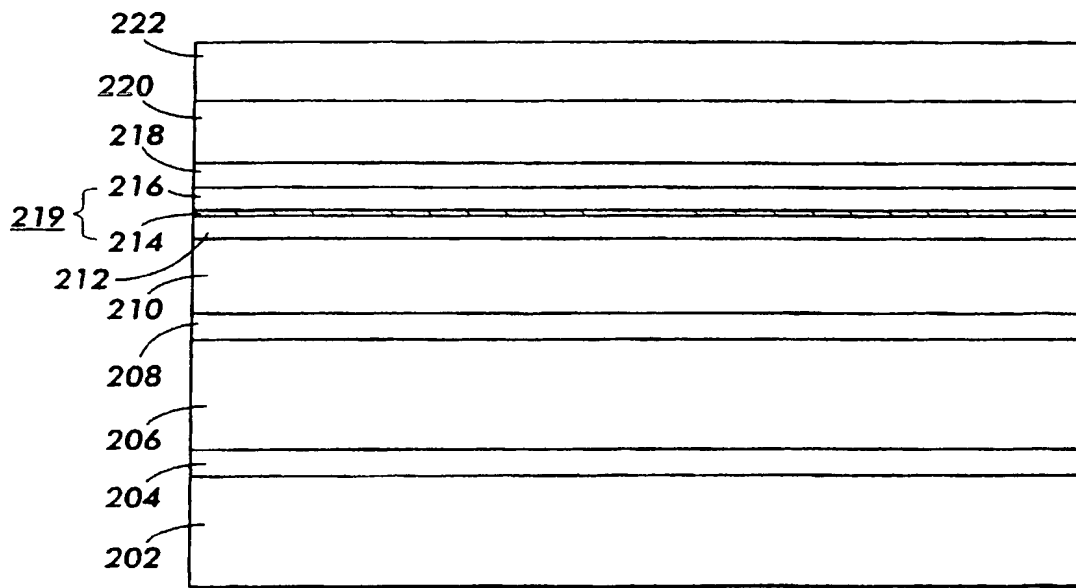


FIG. 3

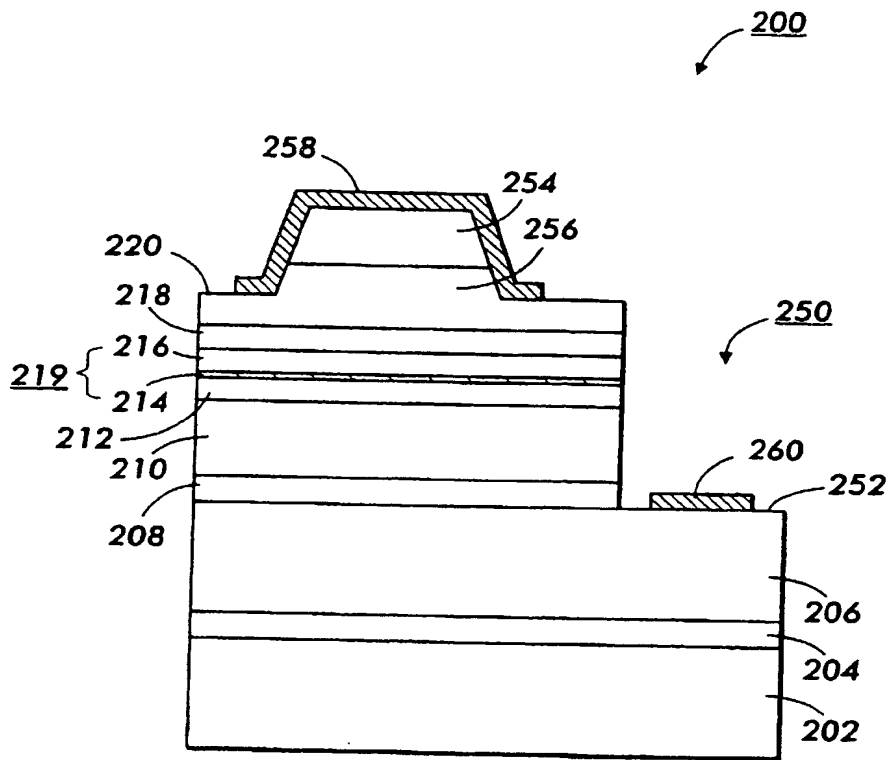
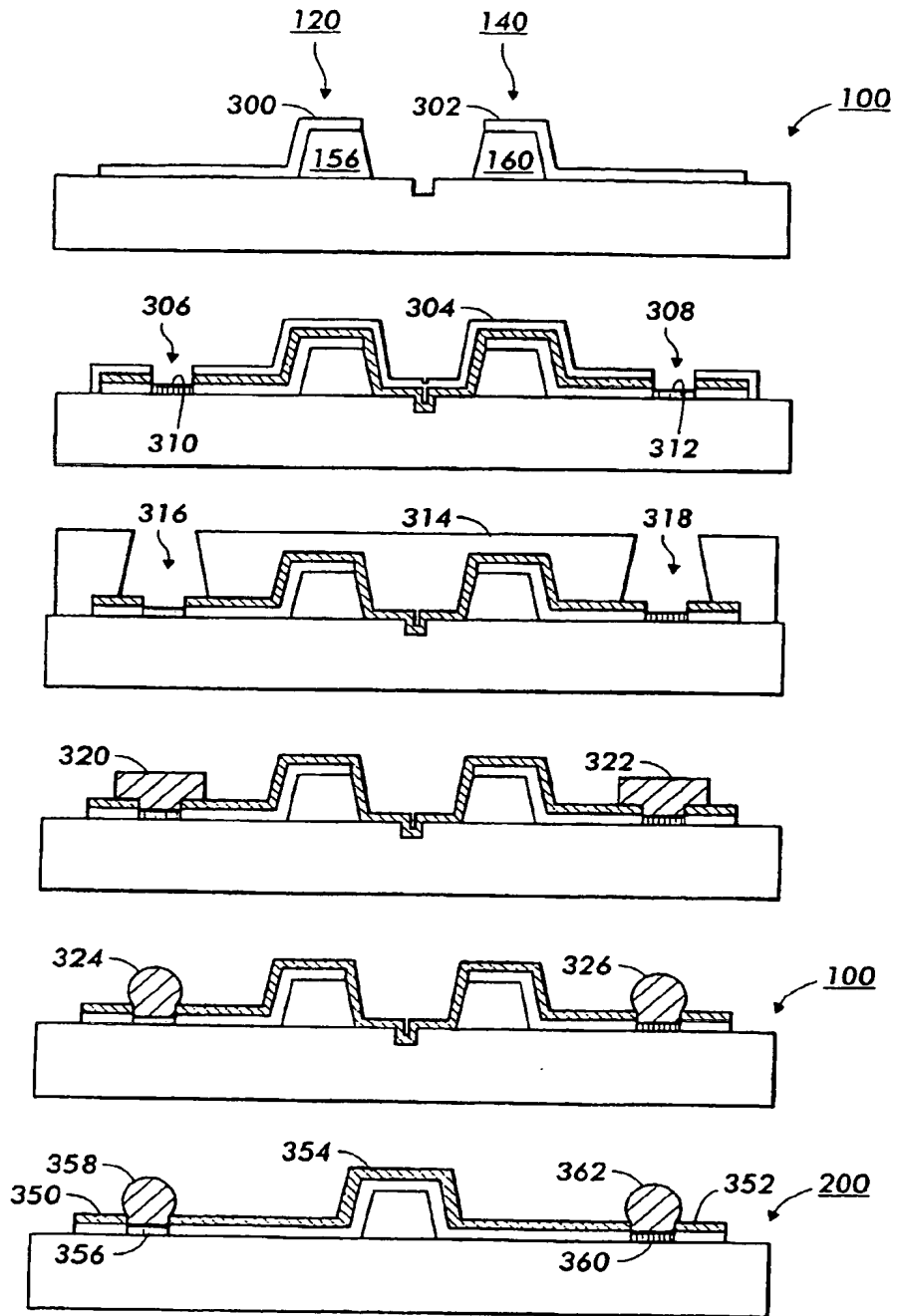


FIG. 4



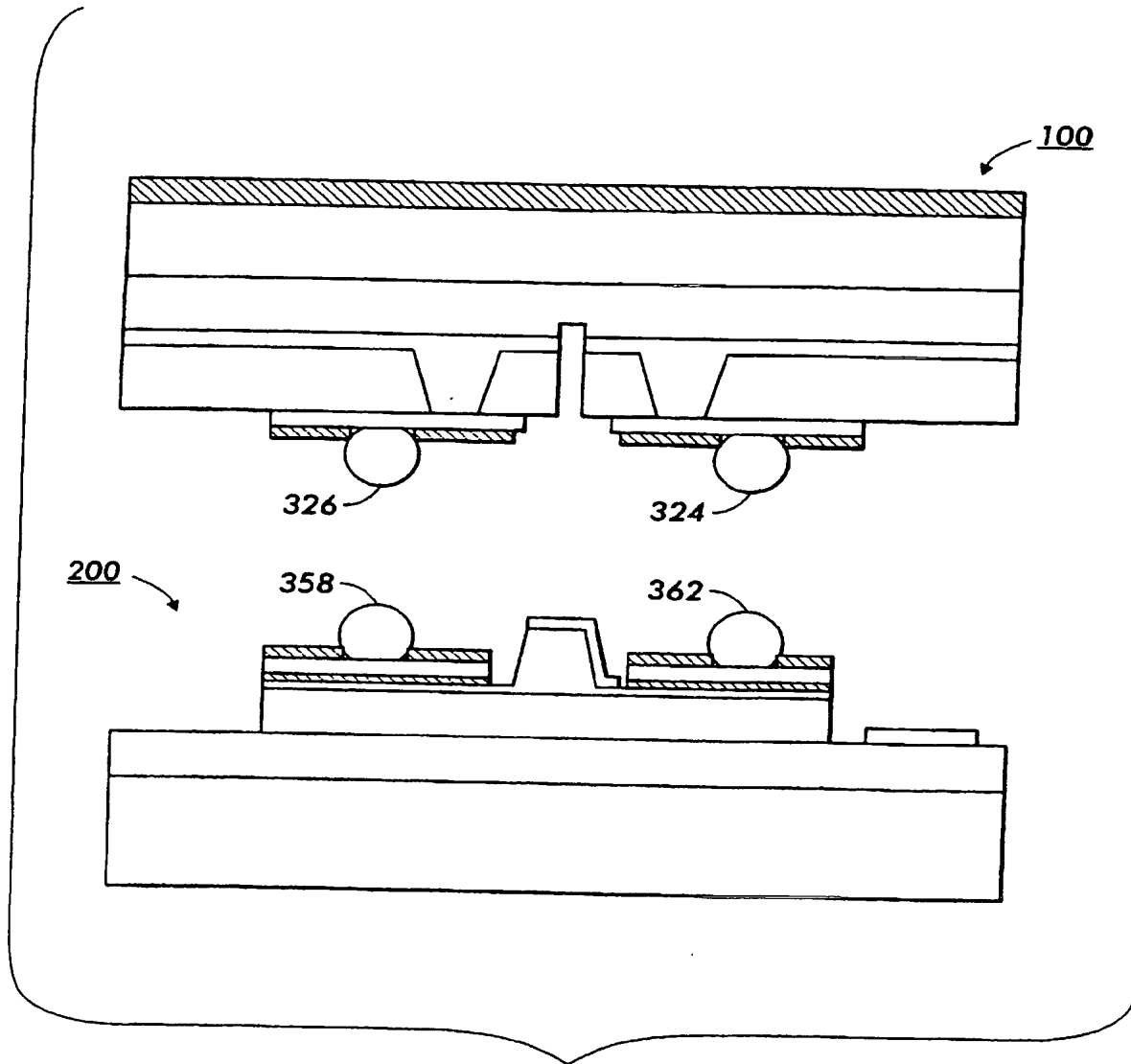
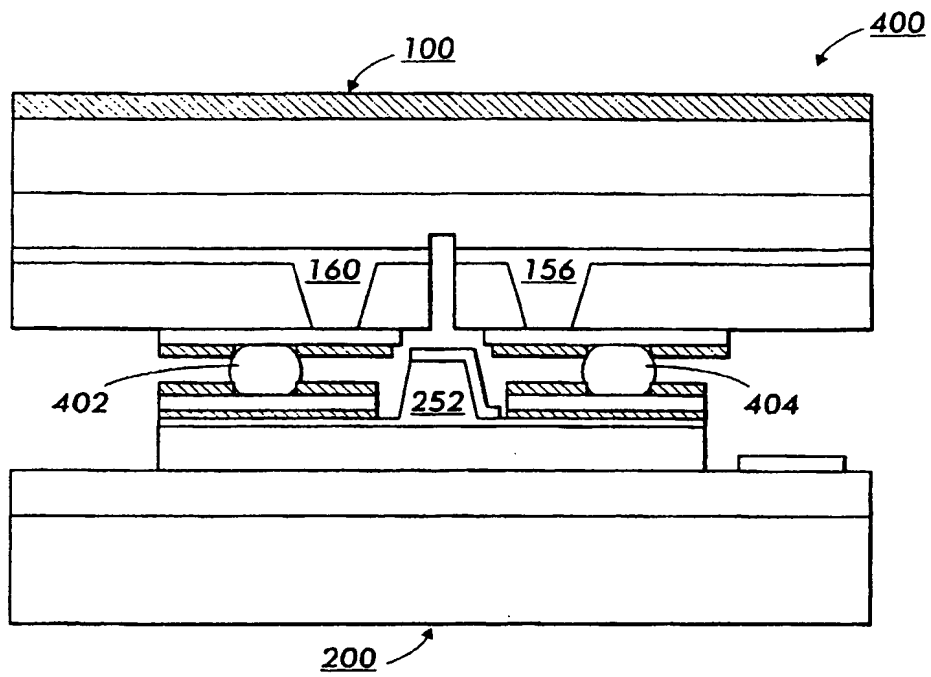


FIG. 6

FIG. 7



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